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Hardware design:
Adopting increasingly
accurate and robust
algorithms often
increases computational
complexity and, hence,

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needs a powerful hardware platform to implement these algorithms. It, in turn, requires us to further improve both system architecture and circuit implementation in order to boost the computing power for real-time operation.

Algorithm and hardware
implementation for

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The theme of the
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platform using Verilog
language. It has been
divided in two sections,

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i.e. Background and

Implementation Of Real Time AES algorithm and its Hardware

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implementation of Bat

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Algorithm

Star tracker is the most accurate attitude sensor that determines satellite direction by applying centroiding algorithm, star identification and attitude determination.

To utilize such algorithms, first, high quality of star images are needed which should be provided through the star tracker camera.

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implementation of star
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In hardware implementation of CRC algorithms such as in software implementation the

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parametric model can be changed by changing Poly, Init, Refln, ReflnOut, XorOut parameters. However unlike a software implementation in hardware each parameter requires I/O FPGA pins which prevent parametric model control.

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A study of hardware implementations of the CRC computation ...

Both digital and analog hardware

implementations of bead sort can achieve a

sorting time of $O(n)$;

however, the

implementation of this

algorithm tends to be

significantly slower in

software and can only

be used to sort lists of

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And Hardware
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positive integers. This is
a perfect example of an
algorithm where the
hardware

implementation is
significantly faster than
the software
implementation is to
contrary to the common
belief that software has
to be faster than
corresponding hardware
(think of mechanical ...

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Radix Sort: An algorithm that works faster with hardware ...

Implementation Of Real Time space. Thus, software implementation has great difficulties. In contrast, hardware encryption has high security, fast speed and strong real-time property. At present, FPGA-based RSA hardware encryption and decryption is a new

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research direction, and an improved study on the existing low-radix Montgomery algorithm has been made in this article.

Improvements of RSA algorithm for hardware encryption ...

Hardware implementation. The schematic for hardware implementation based

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And Hardware Implementation Of Real Time
upon the NSE real-time software algorithm (spectral_estimate_real_time) is shown in Figure 6.

The hardware consists of a mix of analog and digital components, and for simplicity it is shown as sampling the data at exactly 1.0 kHz (1 millisecond intervals) using a 1 kHz clock.

Software algorithm and

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hardware design for real-time ...
efficient hardware
and/or software physical
implementation. Based
on the experience
accumulated in the
process of implementing
a segmentation
algorithm, this thesis
outlines a design for
implementation
methodology comprised
of a development flow

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and associated hardware
Implementation
Design for
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Image Processing
Algorithms

October 25th, 2018 -

By: John Swanson.

Petabytes of data
efficiently travels
between edge devices
and data centers for
processing and
computing of AI

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functions. Accurate and optimized hardware implementations of functions offload many operations that the processing unit would have to execute. As the mathematical algorithms used in AI-based systems evolve, and in some cases stabilize, the demand to implement them in hardware increases, freeing

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compute resources.

Implementation Of Real Time Mathematical

Algorithms In Hardware
For ...

Abstract- This paper describes the hardware implementation methodologies of fixed point binary division algorithms. The implementations have been extended for the

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implementations of digit recurrence and multiplicative based methods have been considered for comparison.

HARDWARE IMPLEMENTATION OF

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Implementation of
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Algorithms and ...

New algorithms (elliptic curves) may reduce application complexity, but still designers must wrestle with the trade-offs between implementing security in software or hardware. Although there will always be exceptions for specific services, niche

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markets, or very small chips, embedded cryptography is replacing software cryptography for a wider range of services.

Cryptography in
Software or Hardware -
It - Maxim Integrated
The other
implementation of a
hardware-accelerated
module is a hardware

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peripheral. Instead of passing data to a software function, you write data to a memory-mapped hardware peripheral. The computation is done outside of the CPU so the CPU can continue running code while the peripheral is working.

Accelerating algorithms
in hardware -

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Implementation
Of Real Time

In computer systems, an algorithm is basically an instance of logic written in software by software developers, to be effective for the intended "target" computer(s) to produce output from given (perhaps null) input. An optimal algorithm, even running in old hardware, would produce faster

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results than a non-optimal (higher time complexity) algorithm for the same purpose, running in more ...