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[Adam] elected to use the Mobile Industry Processor Interface (MIPI) Camera Serial Interface Issue 2 (CSI-2). This high-speed serial interface is optimized for data flowing in one direction.

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MIPI CSI-2 Implementation In FPGAs

The change also updates the "SS" (Slave Select) pin to use "CS" (Chip Select). SPI is widely used in embedded system design and appears in a huge range of devices, with the pin labels ...

Updating The Language Of SPI Pin Labels To Remove Casual References To Slavery

This step crystallizes the efforts of our talents and concretizes our technological lead." Other patent requests, currently under study, will enrich this first series before the end of 2022.

HOPIMUM concretizes its technological lead and files 10 patents for the Hopium Machina

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